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Docket No.: M4065.0767/P767
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Barmak Mansoorian

Application No.: 09/359,056

Confirmation No.: 2286

Filed: July 21, 1999

Art Unit: 2615

For: HIGH RESOLUTION CMOS CIRCUIT
USING A MATCHED IMPEDANCE
OUTPUT TRANSMISSION LINE

Examiner: N. T. Tran

AMENDMENT AFTER FINAL ACTION (37 C.F.R. SECTION 1.116)

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated January 4, 2005 (Paper No. 12222004), finally rejecting claims 1-13, 15 and 16, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 2 of this paper.

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